

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

CIRCUIT AREA MINIMIZATION USING SCALING

Application Number :

Confirmation Number:

First Named Applicant: Michael Gray

Attorney Docket Number: BUR920030123US1

Art Unit:

Examiner:

Search string: (6587992 or 6576147 or 6526555 or 6507931 or 6434721 or 6412100 or 6412097 or 6378114 or 6295633 or 6216252 or 6202193 or 6189132 or 6099584 or 6044208 or 5974244 or 5936868 or 5889681 or 5689433 or 5663892 or 5636132 or 5625568 or 5619419 or 5598344 or 5477467 or 5416722 or 5231590).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6587992	2003-07-01	Marple			
	2	6576147	2003-06-10	Mukai			
	3	6526555	2003-02-25	Teig et al.			
	4	6507931	2003-01-14	Kotani et al.			
	5	6434721	2002-08-13	Chiluvuri et al.			
	6	6412100	2002-06-25	Sasagawa et al.			
	7	6412097	2002-06-25	Kikuchi et al.			
	8	6378114	2002-04-23	Shenoy et al.			
	9	6295633	2001-09-25	Murakawa			
	10	6216252	2001-04-10	Dangelo et al.			
	11	6202193	2001-03-13	Emura et al.			
	12	6189132	2001-02-13	Heng et al.			
	13	6099584	2000-08-08	Arnold et al.			
	14	6044208	2000-03-28	Papadopoulou et al.			
	15	5974244	1999-10-26	Hayashi et al.			
	16	5936868	1999-08-10	Hall			
	17	5889681	1999-03-30	Suda			
	18	5689433	1997-11-18	Edwards			
	19	5663892	1997-09-02	Hayashi et al.			
	20	5636132	1997-06-03	Kamdar			

	21	5625568	1997-04-29	Edwards et al.			
	22	5619419	1997-04-08	D'Haeseleer et al.			
	23	5598344	1997-01-28	Dangelo et al.			
	24	5477467	1995-12-19	Rugg			
	25	5416722	1995-05-16	Edwards			
	26	5231590	1993-07-27	Kumar et al.			

Signature

Examiner Name	Date